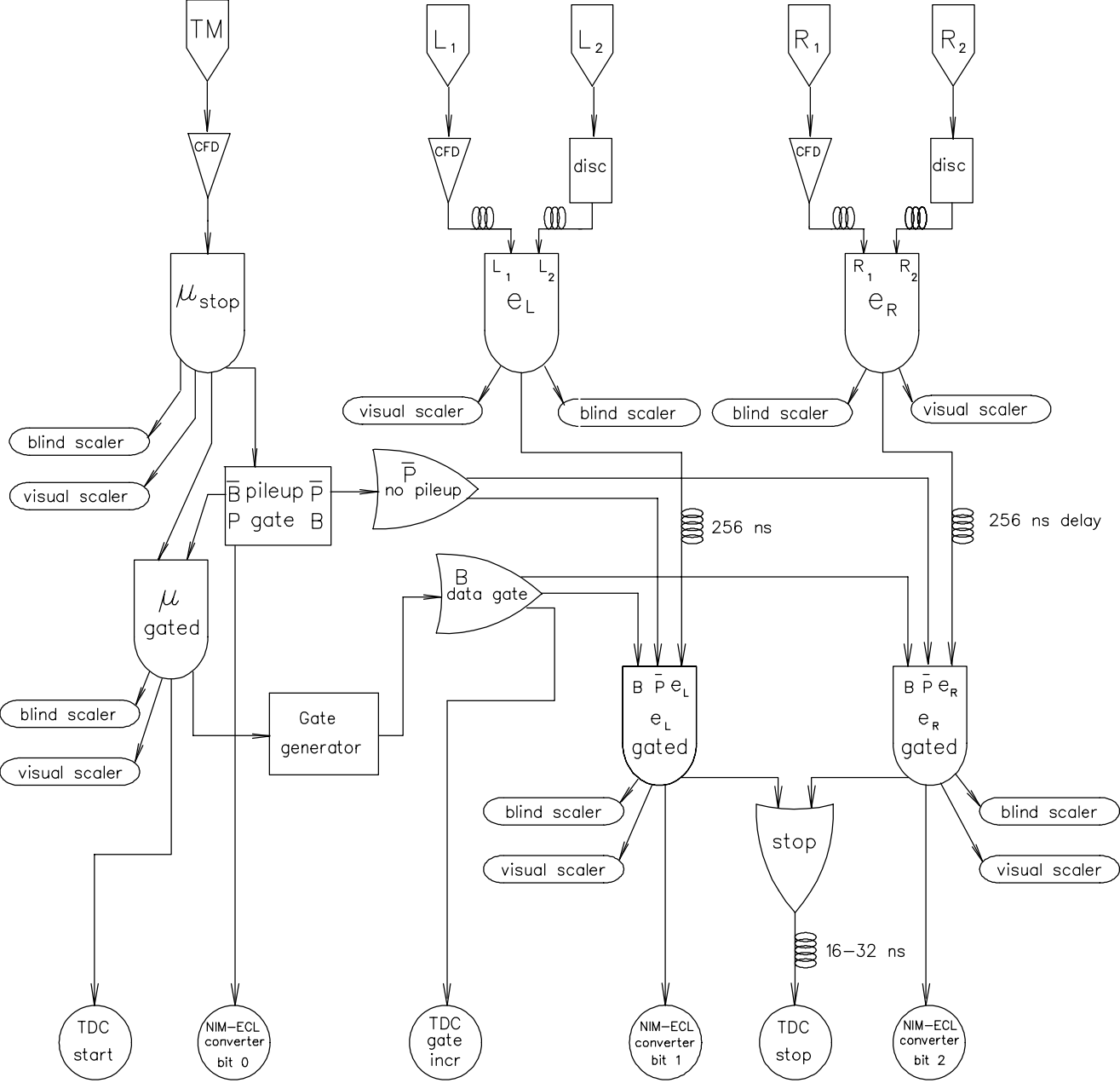
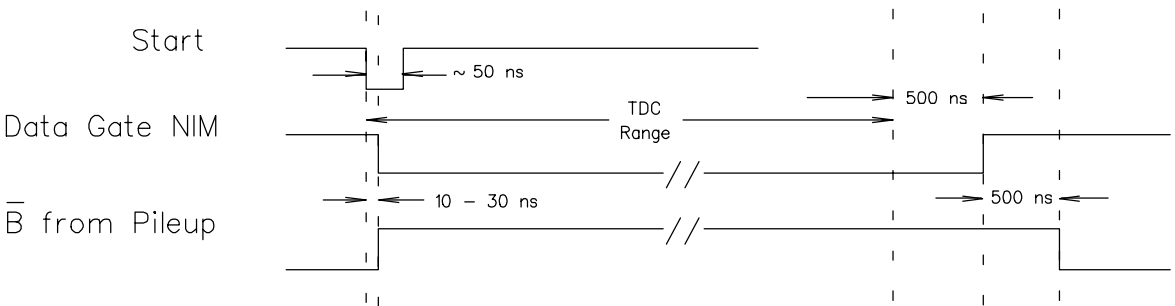

TM
CFD
L_1
L_2
CFD
disc
R_1
R_2
CFD
disc
μ stop
L_1 L_2
e_L
R_1 R_2
e_R
visual scaler
blind scaler
blind scaler
visual scaler
blind scaler
visual scaler
$\bar{B}$ pileup $\bar{P}$
P gate B
$\bar{P}$
no pileup
256 ns
256 ns delay
μ
gated
B
data gate
blind scaler
visual scaler
Gate
generator
B $\bar{P}$ e_L
e_L
gated
B $\bar{P}$ e_R
e_R
gated
blind scaler
visual scaler
stop
blind scaler
visual scaler
16–32 ns
TDC
start
NIM–ECL
converter
bit 0
TDC
gate
incr
NIM–ECL
converter
bit 1
TDC
stop
NIM–ECL
converter
bit 2


Start
~ 50 ns
Data Gate NIM
TDC
Range
500 ns
$\bar{B}$ from Pileup
10 – 30 ns
500 ns